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PATENT  
Docket No.: 13596/3

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Hidefumi TAKAYA, et al.  
SERIAL NO. : 10/573,793  
FILED : 28 March 2006  
FOR : INSULATED GATE TYPE SEMICONDUCTOR DEVICE  
AND MANUFACTURING METHOD THEREOF  
GROUP ART UNIT : (Unassigned)  
EXAMINER : (Unassigned)

COMMISSIONER FOR PATENTS  
Customer Service Window  
Randolph Bldg.  
401 Dulany Street  
Alexandria, VA 22314

**SUBMISSION OF DECLARATION/POWER OF ATTORNEY – NO NOTICE OF  
MISSING REQUIREMENTS RECEIVED**

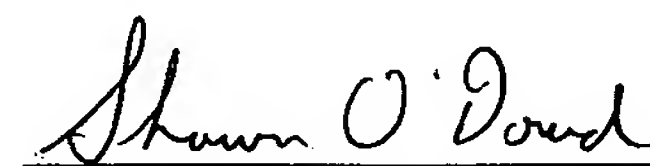
SIR:

While no Notice of Missing Requirements was received in this case, submitted herewith is a copy of the executed Declaration/Power of Attorney.

Please charge Deposit Account No. 11-0600 in the amount of \$130.00 to cover the surcharge for late filing of this document. A copy of this sheet is enclosed for that purpose.

Respectfully submitted,

Dated: 12 September 2006

  
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